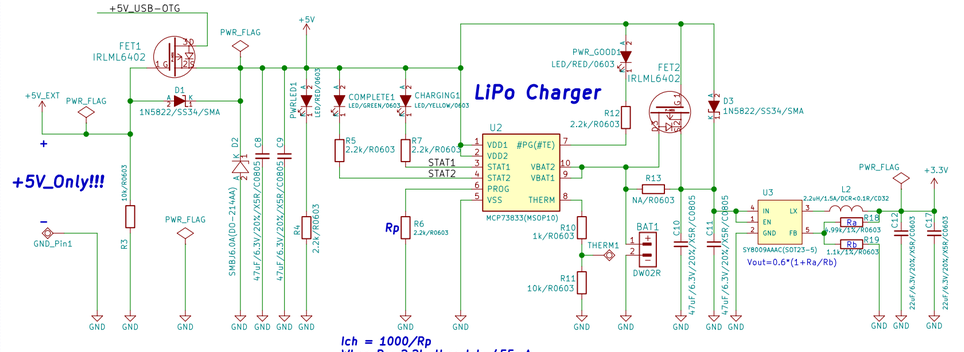
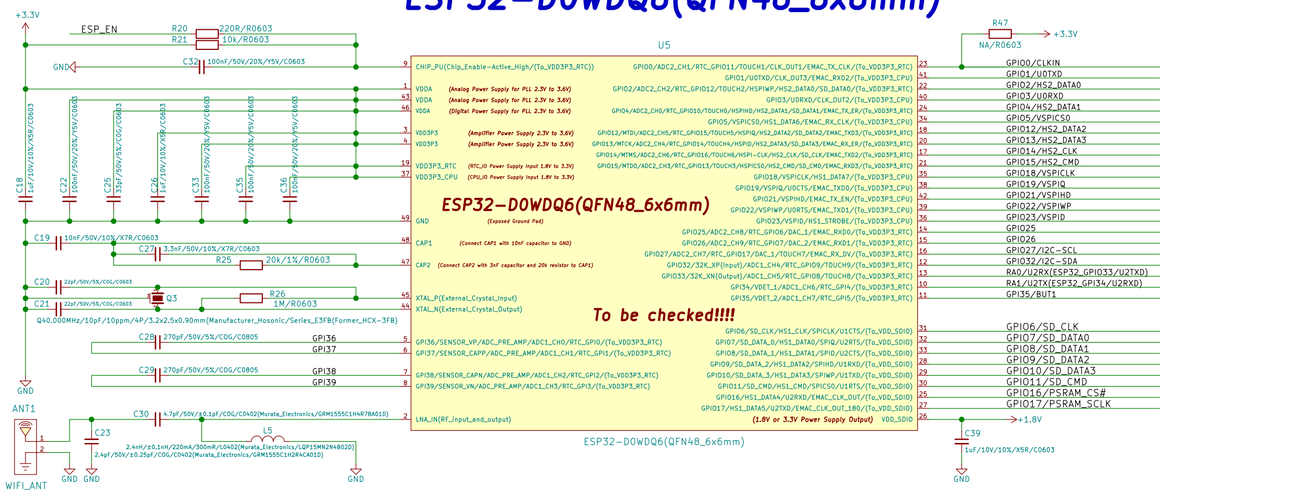
Power Supply

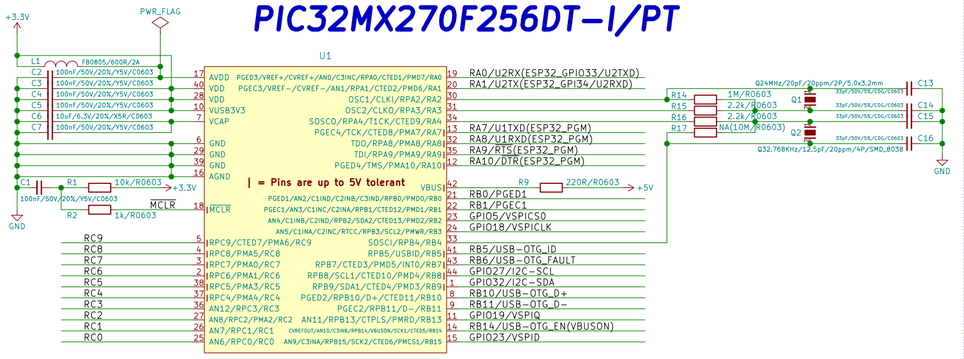


ESP32-D0WDQ6(QFN48_6x6mm)

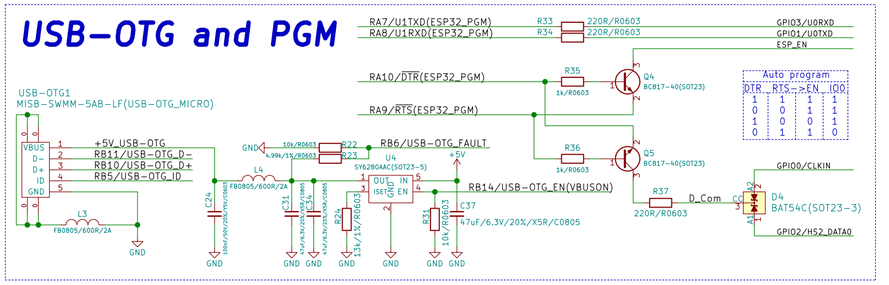


Check the antenna's footprint if OK !!!!!????

PIC32MX270F256DT-I/PT

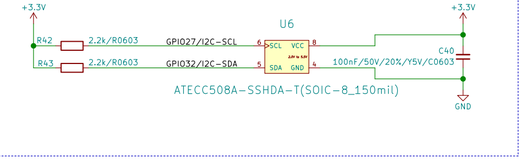


USB-OTG and PGM



Crypto Authentication Device

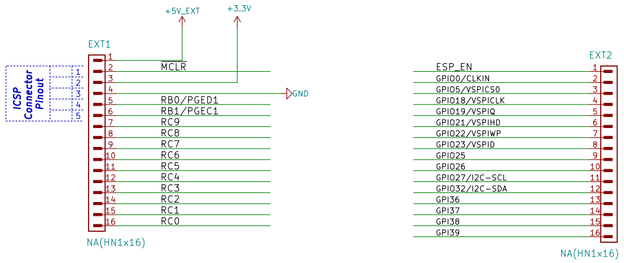
I2C Address: 0xC0



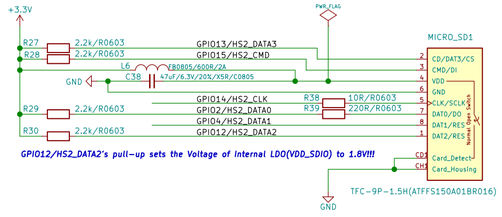
Write donw the PIC registers correct values to set up the selected peripherals!!!!

Optimize the values after all!!!

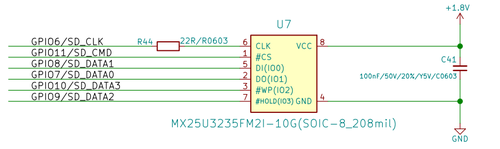
Extension Connectors



SD/MMC Card



32M Bits Serial MULTI I/O FLASH MEMORY



Bootstrapping Pins Informations

Software Selectable Pins

Interface	Signal	Pin
EMAC	EMAC_MDC_out EMAC_MDI_in EMAC_MDO_out EMAC_CRS_in/_out EMAC_COL_out	Any GPIO
I2C	I2CEXT0_SCL_in I2CEXT0_SDA_in I2CEXT1_SCL_in I2CEXT1_SDA_in I2CEXT0_SCL_out I2CEXT0_SDA_out I2CEXT1_SCL_out I2CEXT1_SDA_out	Any GPIO
General Purpose SPI	HSPIQ_in/_out HSPID_in/_out HSPICLK_in/_out HSPI_CS0_in/_out HSPI_CS1_out HSPI_CS2_out VSPIQ_in/_out VSPID_in/_out VSPICLK_in/_out VSPI_CS0_in/_out VSPI_CS1_out VSPI_CS2_out	Any GPIO

For more information refer to esp_wroom_32_datasheet_en.pdf.

Internal Bootstrapping Resistors

MTDI/GPIO12:	Pull-Down
GPIO0:	Pull-Up
GPIO2:	Pull-Down
GPIO4:	Pull-Down
MTDO/GPIO15:	Pull-Up
GPIO5:	Pull-Up

Bootstrapping Pins Settings

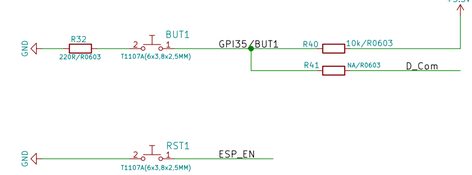
Voltage of Internal LDO(VDD_SDIO)			
Pin	Default	3.3V	1.8V
GPIO12/MTDI	Pull-Down	0	1

Booting Mode			
Pin	Default	SPI Flash Boot	Download Boot
GPIO0	Pull-Up	1	0
GPIO2	Pull-Down	Don't-care	0

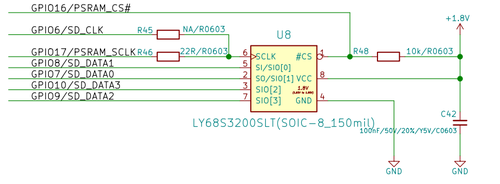
Debugging Log on U0TXD During Booting			
Pin	Default	U0TXD Toggling	U0TXD Silent
GPIO15/MTDO	Pull-Up	1	0

Timing of SDIO Slave					
Pin	Default	Falling-edge Output	Falling-edge Input	Rising-edge Input	Rising-edge Output
GPIO15/MTDO	Pull-Up	0	0	1	1
GPIO5	Pull-Up	0	1	0	1

Buttons



32M Bits Serial Pseudo-SRAM with SPI and QPI



Notes:
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1. According to the datasheet: VSPI Supports Standard SPI, Dual SPI, and Quad SPI that can be connected to the external flash and SRAM!!!

Fiducials

FID1 Fiducial
FID2 Fiducial
FID3 Fiducial
FID4 Fiducial
FID5 Fiducial
FID6 Fiducial

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Title: ESP32-PRO_Rev_A

Size: A2	Date: 2017-06-19	**Rev: A**
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